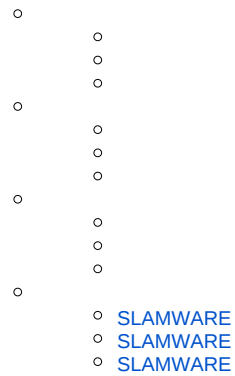


KBSW180102 SLAMWARE Core

SLAMWARE Core



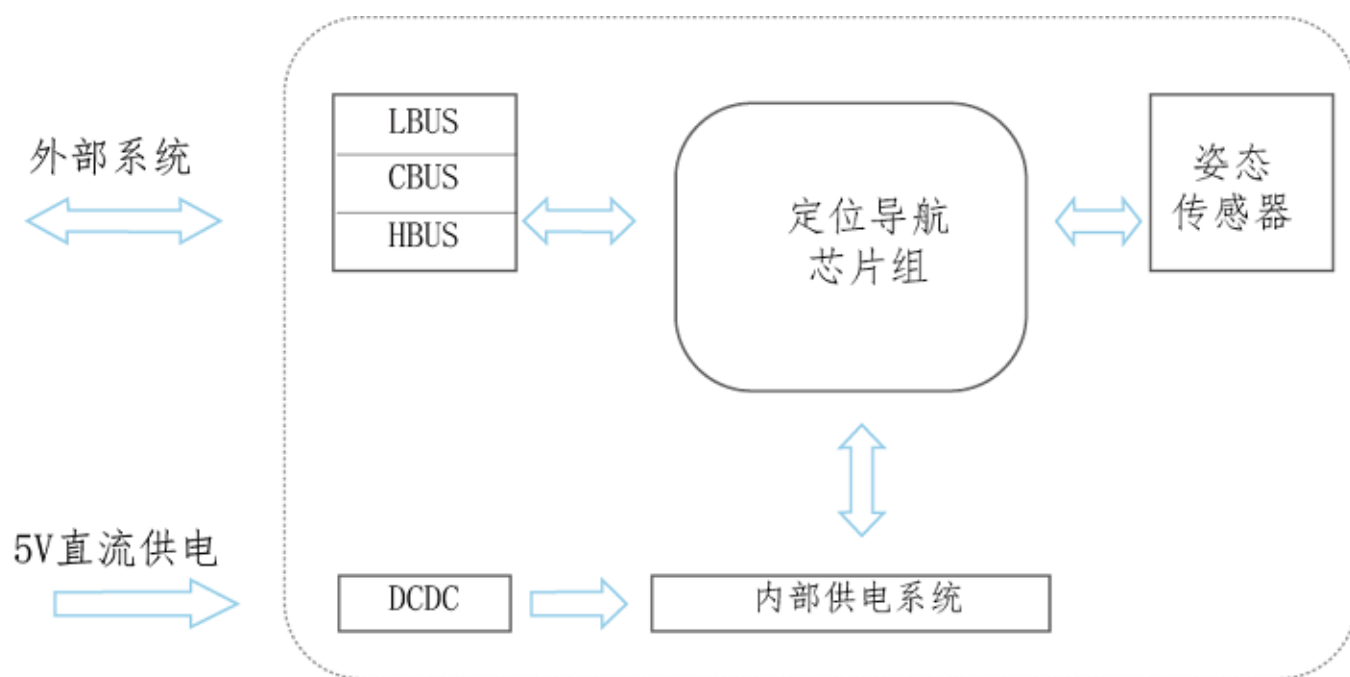
SLAMWARESLAMTECRPLIDARSLAMWARE

- 30mm×51mmMINI PCI-E
-

SLAMWARELBUSRPLIDARCBUSHBUS

5V

SLAMWARE



Slamware 核心模块

	-0.5V ~+6.0V
	-0.3V ~ $V_{sc}+0.3V$
(TA)	-20°C ~+65°C

TA= 20°C

		-	-	-	
V_{DD}		4.75	5	5.25	V
I_{DD}		-	TBD	TBD	mA
V_{DD_IO}		2.9	3.3	3.8	V
I_{DD_IO}		-	-	TBD	mA
V_{DIL}		-	-	$0.2*V_{DD_IO}$	V
V_{DIH}		$0.8*V_{DD_IO}$	-	-	V
V_{DOL}		-	-	$0.2*V_{DD_IO}$	V
V_{DOH}		$0.8*V_{DD_IO}$	-	-	V

I _{STANDBY}	@	-	-	TBD	mA
----------------------	---	---	---	-----	----

TA= 20°C

		.	.	.	
T _{STARTUP}		-	-	TBD	s
-		-	5	-	cm
f _{UART}	UART	-	115200	-	bps
f _{ETH}		-	100	-	Mhz
-		-	-	90000	m ²



1. support@slamtec.com
- 2.

SLAMWAREMINI PCIE52

1	NC	
3	GND	
5	NC	
7	NC	
9	GND	
11	NC	
13	NC	
15	GND	
17	NC	
19	NC	
21	GND	
23	ETH_RX-	HBUS
25	ETH_RX+	HBUS
27	ETH_RREF	HBUS
29	GND	
31	ETH_TX-	HBUS
33	ETH_TX+	HBUS
35	ETH_TREF	HBUS

37	GND	
39	NC	
41	NC	
43	GND	
45	LPWM	LBUS RPLIDARPWM
47	LTX	LBUS RPLIDARRPLIDAR RX
49	LRX	LBUS RPLIDARRPLIDAR TX
51	nL5VEN	LBUS RPLIDAR
2	5V	+5V
4	GND	
6	NC	
8	CBUSY	CBUS OD
10	CRX	CBUS
12	CTX	CBUS
14	GND	
16	nCCMD	CBUS OD
18	GND	
20	NC	
22	NC	
24	MLED	
26	GND	
28	5V	+5V
30	NC	
32	NC	
34	GND	
36	NC	
38	NC	
40	GND	
42	NC	
44	NC	
46	NC	
48	5V	+5V
50	GND	
52	5V	+5V

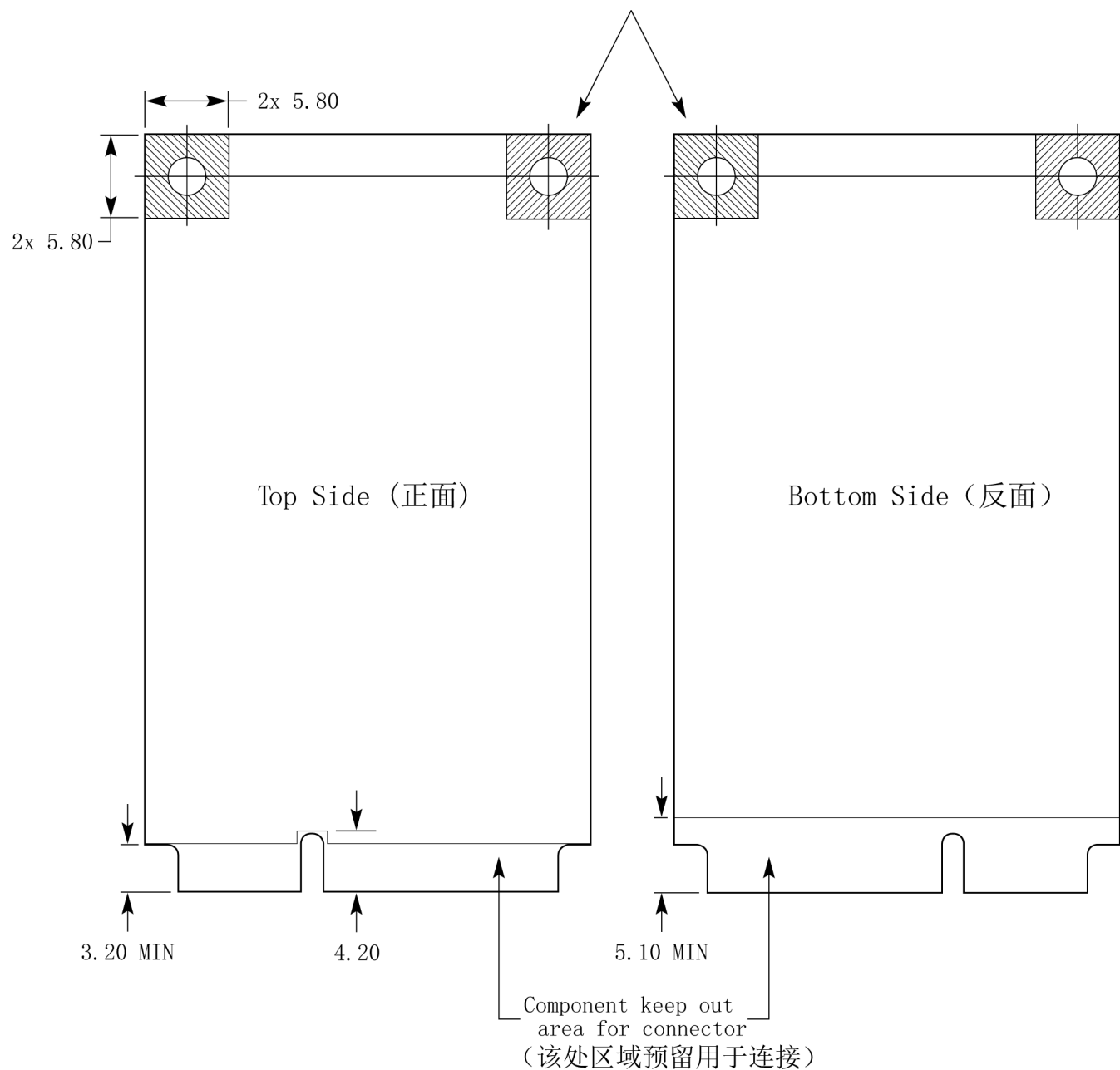
SLAMWARELBUSCBUSHBUSUARTETH

SLAMWAREMINI PCI-E4mm

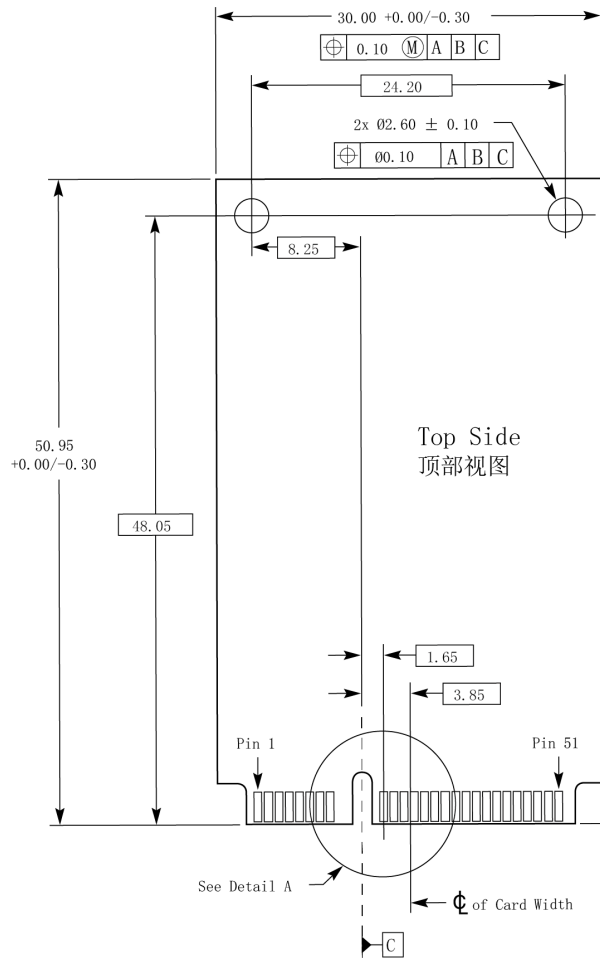
MINI PCI-EMOLEX Edge Card ConnectorPN:67910-90000.80mm Pitch, 9.00mm Height, PCI Express* Mini Card, 52 Circuit, Right Angle, Surface Mount, 0.25µm Gold (Au) Plating, Tape on Reel, Lead-Free

SLAMWARE

Component and routing (all layers) (贯穿所有层)
keep out area for hold down solutions (该处区域预留用于固定PCB板)



SLAMWARE



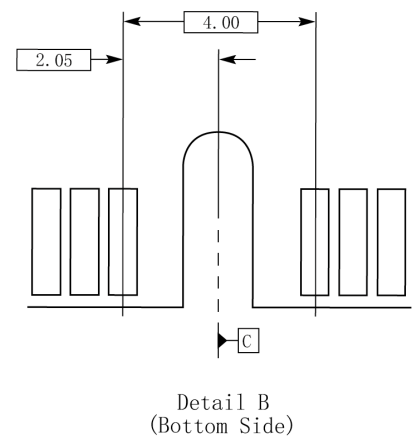
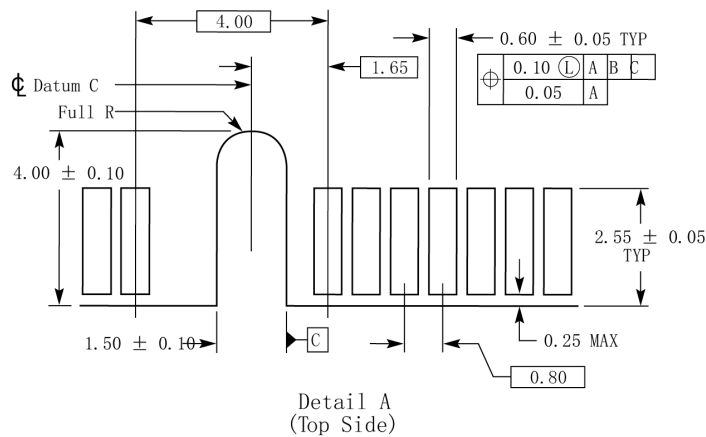
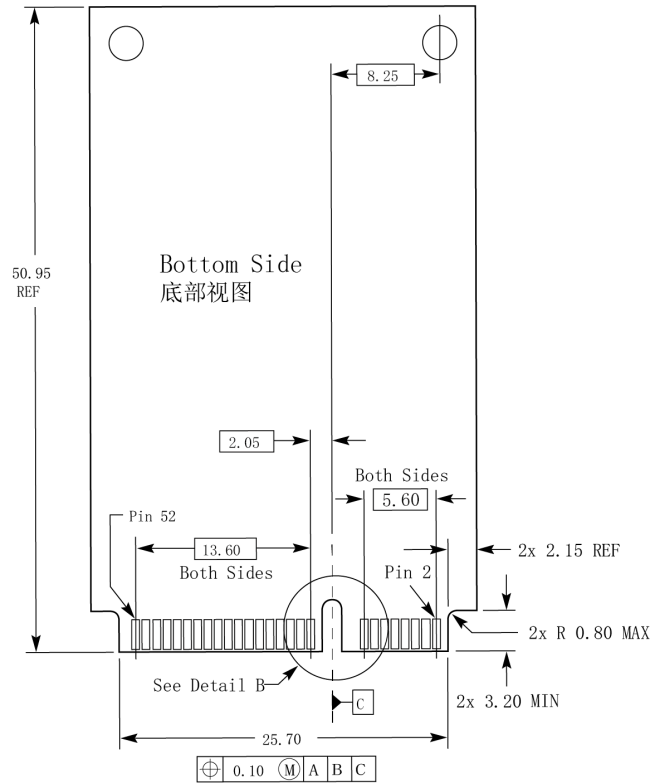
Pin numbering reference (Pin号说明) :

Odd pins - Top Side

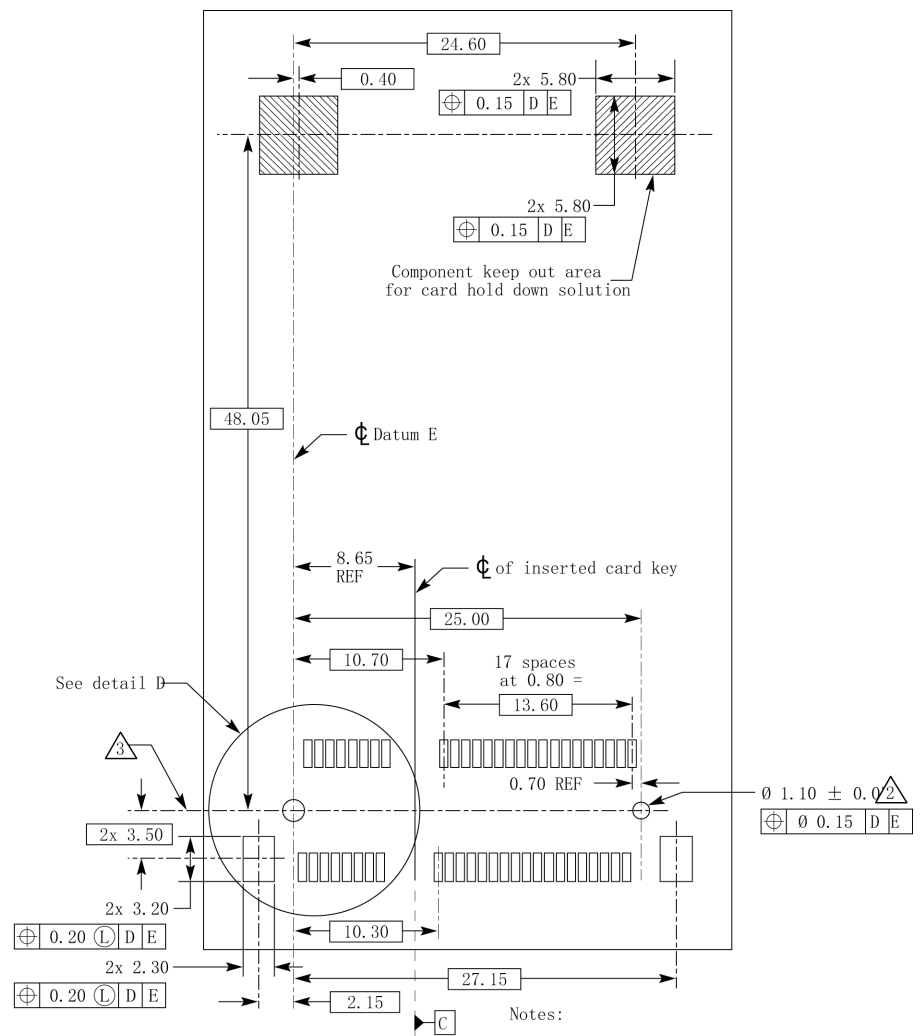
(下图所示顶部视图中Pin号均为奇数, 且Pin号从左往右依次递增)

Even pins - Bottom Side

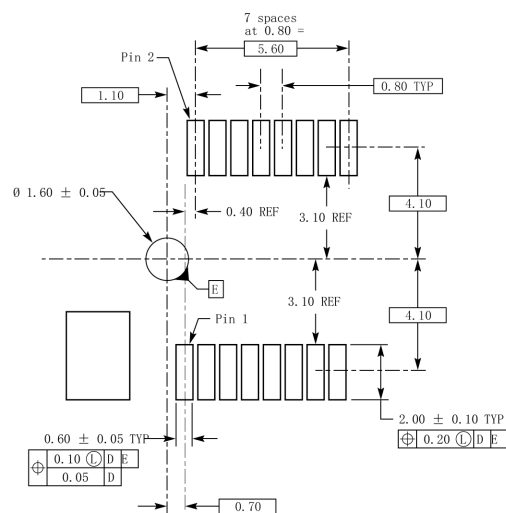
(下图所示底部视图中Pin号均为偶数, 且Pin号从左往右由Pin52至Pin2依次递减)



SLAMWARE



Detail D



- 1 Datum D is the top surface of PCB.
- 2 The horizontal axis for the pattern is established by a line through the center of the $\phi 1.60$ and $\phi 1.10$ holes. The vertical axis is 90° to the horizontal axis, through the center of datum E.
- 3 Location of inserted card edge is aligned with ϕ of holes.